

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Tianhong Zhang

Serial No.: 10/706,486

Filed: November 12, 2003

For: ***METHOD AND APPARATUS FOR
PROGRAMMABLE FIELD EMISSION
DISPLAY***

Group Art Unit: 2629

Examiner: Duc Q Dinh

Examiner's phone: 571-272-7686

Atty. Dkt. No.: 102-0155US1

RESPONSE TO OFFICE ACTION MAILED JUNE 5, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Please reconsider the above-referenced patent application.

As this response is filed within the 3-month period set for response, it is not believed any extension of time fees or other fees are due. However, should any fees be due, this office is authorized to deduct such fees from Deposit Account No. 501922, referencing attorney docket 102-0155US1.

AMENDMENTS TO THE CLAIMS

1. (original) A cathodoluminescent element comprising:
 - at least one emitter;
 - a first transistor having a first terminal coupled said at least one emitter, a second terminal coupled to a ground potential, and a gate terminal, said first transistor being responsive to a first voltage applied to said gate terminal to selectively establish a first conductive path for a first current between said at least one emitter and said ground potential;
 - a programmable element having a first terminal coupled to a first select line to receive a first select signal, a second terminal coupled to said gate terminal of said transistor, and a gate terminal coupled to a second select line, said programmable element being responsive to assertion of a second select signal on said second select line to selectively establish a second conductive path for a second current between said first select line and said gate terminal of said transistor, thereby applying said first voltage to said gate terminal of said transistor;
 - wherein said programmable element comprises a charge storage element for storing a level of electrical charge;
 - and wherein the conductivity of said second conductive path varies in relation to said level of electrical charge such that the magnitude of said first voltage relative to the magnitude of said second select signal varies in relation to said level of electrical charge.
2. (original) A cathodoluminescent element in accordance with claim 1, wherein said programmable element is a floating gate transistor.
3. (original) A cathodoluminescent element in accordance with claim 1, further comprising an infrared-sensitive element disposed along said first current path, the conductivity of said infrared-sensitive element varying in relation to levels of exposure to infrared radiation such that the magnitude of said first current varies in relation to levels of exposure to infrared radiation.

4. (original) A cathodoluminescent element in accordance with claim 3, wherein said infrared sensitive element comprises platinum silicide.

5. (original) A cathodoluminescent element in accordance with claim 1, further comprising a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor establishes said first conductive path.

6. (original) A cathodoluminescent element in accordance with claim 5, further comprising an anode spaced apart from said conductive element, said anode adapted to attract said electrons drawn from said at least one emitter by said conductive element.

7. (original) A cathodoluminescent element in accordance with claim 6, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

8. (original) A cathodoluminescent element in accordance with claim 1, wherein said at least one emitter is disposed on a silicon substrate.

9. (original) A cathodoluminescent element in accordance with claim 7, wherein the intensity of said light emitted from said phosphor layer varies in proportion to levels of infrared radiation to which said infrared sensitive element is exposed.

10. (original) A field emission display, comprising an array of cathodoluminescent elements each comprising:

- at least one emitter;

- a first transistor having a first terminal coupled said at least one emitter, a second terminal coupled to a ground potential, and a gate terminal, said first transistor being responsive to a first voltage applied to said gate terminal to selectively establish a first conductive path for a first current between said at least one emitter and said ground potential;

- a programmable element having a first terminal coupled to a first select line to receive a first select signal, a second terminal coupled to said gate terminal of said transistor, and a gate terminal coupled to a second select line, said programmable element being responsive to assertion of a second select signal on said second select line to selectively establish a second conductive path for a second current between said first select line and said gate terminal of said transistor, thereby applying said first voltage to said gate terminal of said transistor;

- wherein said programmable element comprises a charge storage element for storing a level of electrical charge;

- and wherein the conductivity of said second conductive path varies in relation to said level of electrical charge such that the magnitude of said first voltage relative to the magnitude of said second select signal varies in relation to said level of electrical charge.

11. (original) A field emission display in accordance with claim 10, wherein each said programmable element is a floating gate transistor.

12. (original) A field emission display in accordance with claim 10, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed along said first current path, the conductivity of said infrared-sensitive element varying in relation to levels of exposure to infrared radiation such that the magnitude of said first current varies in relation to levels of exposure to infrared radiation.

13. (original) A field emission display in accordance with claim 10, further comprising, for each cathodoluminescent element, a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor establishes said first conductive path.

14. (original) A field emission display in accordance with claim 13, further comprising an anode spaced apart from said array of cathodoluminescent elements, said anode adapted to attract said electrons drawn from each of said at least one emitter by respective ones of said conductive elements.

15. (original) A field emission display in accordance with claim 14, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

16. (original) A field emission display in accordance with claim 15, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed along said first current path, the conductivity of said infrared-sensitive element varying in relation to levels of exposure to infrared radiation to which it is exposed, such that the magnitude of said first current varies in relation to levels of exposure to infrared radiation to which said infrared-sensitive element is exposed, and such that the intensity of said light emitted by said phosphor layer varies in relation to levels of infrared radiation to which said infrared-sensitive element is exposed.

17. (original) A field emission display in accordance with claim 10, wherein each of said at least one emitter is disposed on a silicon substrate.

18. (original) A method of operating a cathodoluminescent element comprising an emitter selectively coupled to ground in response to application of a predetermined voltage to the gate of a transistor, said method comprising:

- (a) conditioning a programmable element associated with said cathodoluminescent element to level compensate a first select signal to said predetermined voltage level in response to application of a second select signal to a gate terminal of said programmable element;
- (b) applying said level-compensated first select signal to said gate of said transistor.

19. (original) A method in accordance with claim 18, wherein said programmable element is a floating-gate transistor.

20. (original) A method in accordance with claim 19, wherein said step (a) of conditioning comprises storing an electrical charge on the floating gate of said floating gate transistor.

21. (original) A method in accordance with claim 20, further comprising:

- (c) prior to said step (a) of conditioning said programmable element, applying said first select signal to said gate of said transistor without level compensation to cause electrons to be emitted from said emitter; and
- (d) determining a desired magnitude of said stored electrical charge based upon the intensity of electron emission from said emitter while said first select signal is applied to said gate of said transistor.

22. (original) A method of operating a field emission display comprising an array of cathodoluminescent devices each comprising an emitter selectively coupled to ground in response to application of a predetermined voltage to the gate of a transistor, said method comprising:

- (a) for each cathodoluminescent element in said array, conditioning an associated programmable element to level compensate a first select signal to said predetermined voltage level in response to application of a second select signal to a gate terminal of said programmable element;
- (b) applying said level-compensated first select signal to said gate of said transistor in response to application of said second select signal to said gate terminal of said programmable element.

23. (original) A method in accordance with claim 22, wherein said each programmable element is a floating-gate transistor.

24. (original) A method in accordance with claim 23, wherein said step (a) of conditioning comprises storing an electrical charge on the floating gate of said floating gate transistor.

25. (original) A method in accordance with claim 24, further comprising:

- (c) for each cathodoluminescent element in said array, prior to said step (a) of conditioning said programmable element, applying said first select signal to said gate of said transistor without level compensation to cause electrons to be emitted from said emitter; and
- (d) for each cathodoluminescent element in said array, determining a desired magnitude of said stored electrical charge based upon the intensity of electron emission from said emitter while said first select signal is applied to said gate of said transistor.

26. (original) A cathodoluminescent element, comprising:
- at least one emitter;
 - a transistor coupled between said at least one emitter and a ground potential;
 - a programmable element having a first terminal coupled to said gate of said transistor, a second terminal adapted to receive a select signal having a predetermined voltage level, and a third terminal adapted to receive a control signal,
- wherein said programmable element is responsive to activation of said control signal to adjust said predetermined voltage level of said select signal by a preprogrammed amount and apply said adjusted select signal to a gate of said transistor;
- and wherein said transistor is responsive to application of said adjusted select signal to said gate to couple said at least one emitter to said ground potential.
27. (original) A cathodoluminescent element in accordance with claim 26, wherein said programmable element comprises a charge storage device for storing a level of electrical charge thereon.
28. (original) A cathodoluminescent element in accordance with claim 27, wherein said preprogrammed amount is determined by said level of electrical charge.
29. (original) A cathodoluminescent element in accordance with claim 28, wherein said charge storage device comprises a floating gate of a floating gate transistor.
30. (original) A cathodoluminescent element in accordance with claim 26, further comprising a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor couples said at least one emitter to ground.
31. (original) A cathodoluminescent element in accordance with claim 30, further comprising an anode spaced apart from said conductive element, said anode adapted to attract said electrons drawn from said at least one emitter.

32. (original) A cathodoluminescent element in accordance with claim 31, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

33. (original) A cathodoluminescent element in accordance with claim 26, further comprising an infrared-sensitive element disposed between said transistor and said ground potential.

34. (original) A cathodoluminescent element in accordance with claim 32, further comprising an infrared-sensitive element disposed between said transistor and said ground potential.

35. (original) A cathodoluminescent element in accordance with claim 34, wherein the intensity of said light emitted by said anode varies in relation to levels of infrared radiation to which said infrared-sensitive element is exposed.

36. (original) A cathodoluminescent element in accordance with claim 35, wherein said infrared-sensitive element is platinum silicide.

37. (original) A field emission display, comprising an array of cathodoluminescent elements each comprising:

- at least one emitter;

- a transistor coupled between said at least one emitter and a ground potential;

- a programmable element having a first terminal coupled to said gate of said transistor, a second terminal adapted to receive a select signal having a predetermined voltage level, and a third terminal adapted to receive a control signal,

- wherein said programmable element is responsive to activation of said control signal to adjust said predetermined voltage level of said select signal by a preprogrammed amount and apply said adjusted select signal to a gate of said transistor;

- and wherein said transistor is responsive to application of said adjusted select signal to said gate to couple said at least one emitter to said ground potential.

38. (original) A field emission display in accordance with claim 37, wherein said each programmable element comprises a charge storage device for storing a level of electrical charge thereon.

39. (original) A field emission display in accordance with claim 38, wherein said each preprogrammed amount is determined by said level of electrical charge.

40. (original) A field emission display in accordance with claim 39, wherein said charge storage devices comprise floating gates of respective floating gate transistors.

41. (original) A field emission display in accordance with claim 37, further comprising, for each cathodoluminescent element, a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor couples said at least one emitter to ground.

42. (original) A field emission display in accordance with claim 41, further comprising, for each cathodoluminescent element, an anode spaced apart from said conductive element, said anode adapted to attract said electrons drawn from said at least one emitter.

43. (original) A field emission display in accordance with claim 42, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

44. (original) A field emission display in accordance with claim 37, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed between said transistor and said ground potential.

45. (original) A field emission display in accordance with claim 43, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed between said transistor and said ground potential.

46. (original) A field emission display in accordance with claim 45, wherein, for each cathodoluminescent element, the intensity of said light emitted by said anode varies in relation to levels of infrared radiation to which said infrared-sensitive element is exposed.

47. (original) A field emission display in accordance with claim 46, wherein said infrared-sensitive element is platinum silicide.

48. (original) A field emission display in accordance with claim 46, wherein said array of cathodoluminescent elements is adapted to present an image corresponding to infrared radiation levels to which said display is exposed.

49. (original) A field emission display, comprising:
an array of cathodoluminescent elements each being responsive to a separate select signal to emit electrons toward an anode having a phosphor layer applied thereon, such that light is selectively emitted from said phosphor layer; and
for each cathodoluminescent element in said array, a programmable element for determining a level of voltage adjustment to said separate select signal, such that said programmable element determines the intensity of light emitted from said phosphor layer.

50. (original) A field emission display in accordance with claim 49, wherein for each cathodoluminescent element, said programmable element comprises a charge storage device for storing a level of electrical charge thereon.

51. (original) A field emission display in accordance with claim 50, wherein for each cathodoluminescent element, said charge storage device comprises a floating gate of a floating gate transistor.

52. (original) A field emission display in accordance with claim 51, further comprising, for each cathodoluminescent element, an infrared-sensitive element for modulating said intensity of light emitted by said phosphor layer in proportion to levels of infrared radiation to which said each cathodoluminescent element is exposed.

53. (original) A method of operating a field emission display comprising an array of cathodoluminescent elements each having a programmable element associated therewith and each responsive to a separate select signal having a predetermined voltage level to cause light to be emitted from said display at a respective location in said array corresponding to said each cathodoluminescent element, comprising:

- (a) separately for each cathodoluminescent element in said array, pre-programming said programmable element to specify the intensity of light emitted by said display at said respective location in said array corresponding to said each cathodoluminescent element in response to application of said separate select signal corresponding to said each cathodoluminescent element.

54. (original) A method in accordance with claim 53, wherein for each said cathodoluminescent element in said array, said step of pre-programming comprises storing a desired level of electric charge on a charge storage device.

55. (original) A method in accordance with claim 54, wherein said step of storing a desired level of electric charge comprises storing said desired level of electric charge on a floating gate of a floating gate transistor.

56. (currently amended) A field emission display device comprising:

- a ~~photoelectric conversion device including a~~ p-type substrate defining an upper surface;
- an first n-type doped region formed in said p-type substrate at said upper surface of said p-type substrate;
- an second n-type ~~guard ring doped~~ region spaced from said first n-type doped region and formed in said p-type substrate at said upper surface of said p-type substrate;
- and
- an electrically conductive metallic film formed over said upper surface of said p-type substrate and in contact with the first and second n-type doped regions, wherein ~~said p-type substrate and said metallic film are arranged to define a metal-semiconductor Schottky barrier~~;
- an electrically conductive grid structure;
- an electrically conductive anode structure; and
- an electron emitter conductively coupled to said ~~photoelectric conversion device~~ first n-type doped region, wherein said at least one electron emitter and said grid structure are displaced from said anode structure across a field emission region, ~~and wherein said field emission region is defined in a vacuum.~~

57. (currently amended) A field emission display device as claimed in claim 56 wherein said electron emitter is formed over said first n-type doped region ~~of said upper surface of said p-type substrate~~.

58. (currently amended) A field emission display device as claimed in claim 57 wherein said electron emitter is formed integrally with said first n-type doped region ~~of said upper surface of said p-type substrate~~.

59. (currently amended) A field emission display device as claimed in claim 56 wherein said electron emitter comprises a tip ~~defining an emission apex~~.

60. (currently amended) A field emission display device as claimed in claim 56 wherein said emitter comprises a plurality of tips defining respective emission apexes anode structure comprises a phosphor coated screen.

61. (currently amended) A field emission display device as claimed in claim 56 wherein said metallic film is platinum silicide ~~formed over at least a portion of said n-type doped region of said upper surface of said p-type substrate and over at least a portion of said n-type guard ring region of said upper surface of said p-type substrate.~~

62. (currently amended) A field emission display device as claimed in claim 56 wherein said metallic film and said p-type substrate comprise an infra-red-sensitive junction ~~comprising:~~

~~a photoelectric conversion device including
a p-type silicon substrate defining an upper surface,
an n-type doped region formed in said p-type silicon substrate at said upper surface of said p-type substrate,
an n-type guard ring region spaced from and surrounding said n-type doped region and formed in said p-type silicon substrate at said upper surface of said p-type substrate,
and
a platinum silicide metallic film formed over said upper surface of said p-type substrate, wherein said p-type substrate and said metallic film are arranged to define a metal-semiconductor Schottky barrier, and wherein said metallic film extends over an interior circumferential portion of said guard ring region and an exterior circumferential portion of said n-type doped region;
an electrically conductive grid structure;
an electrically conductive anode structure including a phosphor screen; and
a plurality of electron emitter tips integrally formed with said n-type doped region and conductively coupled to said photoelectric conversion device via said platinum silicide metallic film, wherein said electron emitter tips and said grid structure are displaced from said anode structure across a field emission region, and wherein said field emission region is defined in a vacuum.~~

63. (currently amended) A field emission display device as claimed in claimed 56 further comprising a dielectric layer between the grid and the metallic filmcomprising:

~~a photoelectric conversion device including~~
~~a p-type silicon substrate defining an upper surface and a plurality of emitter tip profiles including respective emission apices,~~
~~an n-type guard ring region spaced from and surrounding said plurality of emitter tip profiles and formed in said p-type silicon substrate at said upper surface of said p-type substrate, and~~
~~a platinum silicide metallic film formed over said upper surface of said p-type substrate and said emitter tip profiles, wherein said p-type substrate and said metallic film are arranged to define a metal-semiconductor Schottky barrier, and wherein said metallic film extends over an interior circumferential portion of said guard ring region;~~
~~a silicon dioxide dielectric layer formed over a portion of said metallic film spaced from said plurality of emitter tip profiles;~~
~~an electrically conductive grid structure separated from said metallic film and said substrate by said dielectric layer and arranged to define a portion of an emitter tip void spaced from and surrounding said plurality of emitter tip profiles; and~~
~~an electrically conductive anode structure including a phosphor coated screen, said anode structure being spaced from said grid structure and said plurality of electron emitter tip profiles to define a field emission region, wherein said field emission region is defined in a vacuum.~~

REMARKS

Claims 1-63 are pending. Claims 1-55 have been allowed. A clean copy of the claims as amended herein is attached for the convenience of the Examiner.

I. Assignee Consent:

The Examiner has objected to the Assignee's consent to reissue paper because the person who signed the submission failed to expressly state that he was authorized to sign on behalf on the assignee, Micron Technology, Inc. Applicant remedies this by submitting new versions of (1) a Rule 3.73(b) Statement, and (2) a "Reissue Application: Consent of Assignee" paper, which are attached.

II. Drawings:

The Examiner objected to the drawings for not depicting all of the limitations of claims, and in particular was concerned that the "Schottky barrier" limitation was not illustrated in the Figures. However, in light of the amendments made to the claims, which include deletion of the "Schottky barrier" limitation, Applicant assumes the Examiner would agree that all claimed limitations are now depicted in (e.g.) Figure 7, and therefore that amendment to the Figures is unnecessary.

III. Written Description Rejection:

Claims 56-63 have been rejected under 35 U.S.C. § 112, ¶ 1 for lacking a written description of the invention as claimed. Specifically, the Examiner has suggested that particular limitations in each of the claims lack support in Applicant's specification.

In response, Applicant has amended claim 56, which was specifically cited by the Examiner as having an insufficient written description. (Claims 62 and 63, also cited by the Examiner, have now been amended to depend from claim 56). So amended, claim 56 is now fully supported by the specification, as are all of the dependent claims. Indeed, most of the support can be found in Figure 7 and its supporting text (col. 3, l. 61 to col. 4, l. 53), which provides "a cross-sectional view of a cathodoluminescent element 10 in accordance with one embodiment of the invention." Citation to original USP 6,366,266, col. 3, ll. 46-48. To prove support, citations to the specification (i.e., the '266 patent) are made for claims 56-63 as amended:

56. A field emission display device comprising:
- a p-type substrate [**Fig. 7; element 12**] defining an upper surface;
 - a first n-type doped region [**Fig. 7; element 14**] formed in said p-type substrate at said upper surface of said p-type substrate;
 - a second n-type doped region [**Fig. 7; element 16**] spaced from said first n-type doped region and formed in said p-type substrate at said upper surface of said p-type substrate;
 - an electrically conductive metallic film [**Figs. 5 and 7; element 22**] formed over said upper surface of said p-type substrate and in contact with the first and second n-type doped regions;
 - an electrically conductive grid [**Fig. 7; element 24**];
 - an electrically conductive anode structure [**Fig. 7; element 30/32**]; and
 - an electron emitter [**Fig. 7; element 18**] conductively coupled to said first n-type doped region [**Fig. 7; element 14**], wherein said at least one electron emitter [**Fig. 7; element 18**] and said grid [**Fig. 7; element 24**] are displaced from said anode structure [**Fig. 7; element 30/32**] across a field emission region.
57. A field emission display device as claimed in claim 56 wherein said electron emitter [**Fig. 7; element 18**] is formed over said first n-type doped region [**Fig. 7; element 14**].

58. A field emission display device as claimed in claim 57 wherein said electron emitter **[Fig. 7; element 18]** is formed integrally with said first n-type doped region **[Fig. 7; element 14]**.

59. A field emission display device as claimed in claim 56 wherein said electron emitter **[Fig. 7; element 14]** comprises a tip **[col. 3, l. 64]**.

60. A field emission display device as claimed in claim 56 wherein said anode structure comprises a phosphor coated screen **[col. 4, ll. 26-30]**.

61. A field emission display device as claimed in claim 56 wherein said metallic film is platinum silicide **[col. 4, ll. 4-6]**.

62. A field emission display device as claimed in claim 56 wherein said metallic film and said p-type substrate comprise an infra-red-sensitive junction **[col. 4, ll. 6-8]**.

63. A field emission display device as claimed in claim 56 further comprising a dielectric layer **[Fig. 7; element 26]** between the grid **[Fig. 7; element 24]** and the metallic film **[Fig. 7; element 22]**.

In short, claims 56-63 as amended are fully supported by the specification, and fully compliant with the written description requirement of 35 U.S.C. § 112, ¶ 1.

* * * * *

Based on the above remarks, Applicant respectfully submits that pending claims 1-63 are allowable, and requests that a Notice of Allowance issue for these claims.

Respectfully submitted,

/TGL/
Terril Lewis, Reg. No. 46,065

Date: August 28, 2006
CUSTOMER NO. 29855
Wong, Cabello, Lutsch,
Rutherford & Brucculeri, L.L.P..
20333 SH 249. Suite 600
Houston, TX 77070
832/446-2405
Fax 832/446-2424

10/706,486
Convenience Clean Copy of Pending Claims

1. (original) A cathodoluminescent element comprising:
 - at least one emitter;
 - a first transistor having a first terminal coupled said at least one emitter, a second terminal coupled to a ground potential, and a gate terminal, said first transistor being responsive to a first voltage applied to said gate terminal to selectively establish a first conductive path for a first current between said at least one emitter and said ground potential;
 - a programmable element having a first terminal coupled to a first select line to receive a first select signal, a second terminal coupled to said gate terminal of said transistor, and a gate terminal coupled to a second select line, said programmable element being responsive to assertion of a second select signal on said second select line to selectively establish a second conductive path for a second current between said first select line and said gate terminal of said transistor, thereby applying said first voltage to said gate terminal of said transistor;
 - wherein said programmable element comprises a charge storage element for storing a level of electrical charge;
 - and wherein the conductivity of said second conductive path varies in relation to said level of electrical charge such that the magnitude of said first voltage relative to the magnitude of said second select signal varies in relation to said level of electrical charge.
2. (original) A cathodoluminescent element in accordance with claim 1, wherein said programmable element is a floating gate transistor.
3. (original) A cathodoluminescent element in accordance with claim 1, further comprising an infrared-sensitive element disposed along said first current path, the conductivity of said infrared-

sensitive element varying in relation to levels of exposure to infrared radiation such that the magnitude of said first current varies in relation to levels of exposure to infrared radiation.

4. (original) A cathodoluminescent element in accordance with claim 3, wherein said infrared sensitive element comprises platinum silicide.

5. (original) A cathodoluminescent element in accordance with claim 1, further comprising a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor establishes said first conductive path.

6. (original) A cathodoluminescent element in accordance with claim 5, further comprising an anode spaced apart from said conductive element, said anode adapted to attract said electrons drawn from said at least one emitter by said conductive element.

7. (original) A cathodoluminescent element in accordance with claim 6, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

8. (original) A cathodoluminescent element in accordance with claim 1, wherein said at least one emitter is disposed on a silicon substrate.

9. (original) A cathodoluminescent element in accordance with claim 7, wherein the intensity of said light emitted from said phosphor layer varies in proportion to levels of infrared radiation to which said infrared sensitive element is exposed.

10. (original) A field emission display, comprising an array of cathodoluminescent elements each comprising:

at least one emitter;

a first transistor having a first terminal coupled said at least one emitter, a second terminal coupled to a ground potential, and a gate terminal, said first transistor being responsive to a first voltage applied to said gate terminal to selectively

establish a first conductive path for a first current between said at least one emitter and said ground potential;

a programmable element having a first terminal coupled to a first select line to receive a first select signal, a second terminal coupled to said gate terminal of said transistor, and a gate terminal coupled to a second select line, said programmable element being responsive to assertion of a second select signal on said second select line to selectively establish a second conductive path for a second current between said first select line and said gate terminal of said transistor, thereby applying said first voltage to said gate terminal of said transistor;

wherein said programmable element comprises a charge storage element for storing a level of electrical charge;

and wherein the conductivity of said second conductive path varies in relation to said level of electrical charge such that the magnitude of said first voltage relative to the magnitude of said second select signal varies in relation to said level of electrical charge.

11. (original) A field emission display in accordance with claim 10, wherein each said programmable element is a floating gate transistor.

12. (original) A field emission display in accordance with claim 10, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed along said first current path, the conductivity of said infrared-sensitive element varying in relation to levels of exposure to infrared radiation such that the magnitude of said first current varies in relation to levels of exposure to infrared radiation.

13. (original) A field emission display in accordance with claim 10, further comprising, for each cathodoluminescent element, a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor establishes said first conductive path.

14. (original) A field emission display in accordance with claim 13, further comprising an anode spaced apart from said array of cathodoluminescent elements, said anode adapted to attract said electrons drawn from each of said at least one emitter by respective ones of said conductive elements.

15. (original) A field emission display in accordance with claim 14, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

16. (original) A field emission display in accordance with claim 15, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed along said first current path, the conductivity of said infrared-sensitive element varying in relation to levels of exposure to infrared radiation to which it is exposed, such that the magnitude of said first current varies in relation to levels of exposure to infrared radiation to which said infrared-sensitive element is exposed, and such that the intensity of said light emitted by said phosphor layer varies in relation to levels of infrared radiation to which said infrared-sensitive element is exposed.

17. (original) A field emission display in accordance with claim 10, wherein each of said at least one emitter is disposed on a silicon substrate.

18. (original) A method of operating a cathodoluminescent element comprising an emitter selectively coupled to ground in response to application of a predetermined voltage to the gate of a transistor, said method comprising:

- (a) conditioning a programmable element associated with said cathodoluminescent element to level compensate a first select signal to said predetermined voltage level in response to application of a second select signal to a gate terminal of said programmable element;
- (b) applying said level-compensated first select signal to said gate of said transistor.

19. (original) A method in accordance with claim 18, wherein said programmable element is a floating-gate transistor.

20. (original) A method in accordance with claim 19, wherein said step (a) of conditioning comprises storing an electrical charge on the floating gate of said floating gate transistor.

21. (original) A method in accordance with claim 20, further comprising:

- (c) prior to said step (a) of conditioning said programmable element, applying said first select signal to said gate of said transistor without level compensation to cause electrons to be emitted from said emitter; and
- (d) determining a desired magnitude of said stored electrical charge based upon the intensity of electron emission from said emitter while said first select signal is applied to said gate of said transistor.

22. (original) A method of operating a field emission display comprising an array of cathodoluminescent devices each comprising an emitter selectively coupled to ground in response to application of a predetermined voltage to the gate of a transistor, said method comprising:

- (a) for each cathodoluminescent element in said array, conditioning an associated programmable element to level compensate a first select signal to said predetermined voltage level in response to application of a second select signal to a gate terminal of said programmable element;
- (b) applying said level-compensated first select signal to said gate of said transistor in response to application of said second select signal to said gate terminal of said programmable element.

23. (original) A method in accordance with claim 22, wherein said each programmable element is a floating-gate transistor.

24. (original) A method in accordance with claim 23, wherein said step (a) of conditioning comprises storing an electrical charge on the floating gate of said floating gate transistor.

25. (original) A method in accordance with claim 24, further comprising:

- (c) for each cathodoluminescent element in said array, prior to said step (a) of conditioning said programmable element, applying said first select signal to said gate of said transistor without level compensation to cause electrons to be emitted from said emitter; and
- (d) for each cathodoluminescent element in said array, determining a desired magnitude of said stored electrical charge based upon the intensity of electron emission from said emitter while said first select signal is applied to said gate of said transistor.

26. (original) A cathodoluminescent element, comprising:

- at least one emitter;
 - a transistor coupled between said at least one emitter and a ground potential;
 - a programmable element having a first terminal coupled to said gate of said transistor, a second terminal adapted to receive a select signal having a predetermined voltage level, and a third terminal adapted to receive a control signal,
- wherein said programmable element is responsive to activation of said control signal to adjust said predetermined voltage level of said select signal by a preprogrammed amount and apply said adjusted select signal to a gate of said transistor;
- and wherein said transistor is responsive to application of said adjusted select signal to said gate to couple said at least one emitter to said ground potential.

27. (original) A cathodoluminescent element in accordance with claim 26, wherein said programmable element comprises a charge storage device for storing a level of electrical charge thereon.

28. (original) A cathodoluminescent element in accordance with claim 27, wherein said preprogrammed amount is determined by said level of electrical charge.

29. (original) A cathodoluminescent element in accordance with claim 28, wherein said charge storage device comprises a floating gate of a floating gate transistor.

30. (original) A cathodoluminescent element in accordance with claim 26, further comprising a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor couples said at least one emitter to ground.

31. (original) A cathodoluminescent element in accordance with claim 30, further comprising an anode spaced apart from said conductive element, said anode adapted to attract said electrons drawn from said at least one emitter.

32. (original) A cathodoluminescent element in accordance with claim 31, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

33. (original) A cathodoluminescent element in accordance with claim 26, further comprising an infrared-sensitive element disposed between said transistor and said ground potential.

34. (original) A cathodoluminescent element in accordance with claim 32, further comprising an infrared-sensitive element disposed between said transistor and said ground potential.

35. (original) A cathodoluminescent element in accordance with claim 34, wherein the intensity of said light emitted by said anode varies in relation to levels of infrared radiation to which said infrared-sensitive element is exposed.

36. (original) A cathodoluminescent element in accordance with claim 35, wherein said infrared-sensitive element is platinum silicide.

37. (original) A field emission display, comprising an array of cathodoluminescent elements each comprising:

at least one emitter;

a transistor coupled between said at least one emitter and a ground potential;

a programmable element having a first terminal coupled to said gate of said transistor, a second terminal adapted to receive a select signal having a predetermined voltage level, and a third terminal adapted to receive a control signal,

wherein said programmable element is responsive to activation of said control signal to adjust said predetermined voltage level of said select signal by a preprogrammed amount and apply said adjusted select signal to a gate of said transistor;

and wherein said transistor is responsive to application of said adjusted select signal to said gate to couple said at least one emitter to said ground potential.

38. (original) A field emission display in accordance with claim 37, wherein said each programmable element comprises a charge storage device for storing a level of electrical charge thereon.

39. (original) A field emission display in accordance with claim 38, wherein said each preprogrammed amount is determined by said level of electrical charge.

40. (original) A field emission display in accordance with claim 39, wherein said charge storage devices comprise floating gates of respective floating gate transistors.

41. (original) A field emission display in accordance with claim 37, further comprising, for each cathodoluminescent element, a conductive element proximal to said at least one emitter, said conductive element adapted to draw electrons from said at least one emitter when said transistor couples said at least one emitter to ground.

42. (original) A field emission display in accordance with claim 41, further comprising, for each cathodoluminescent element, an anode spaced apart from said conductive element, said anode adapted to attract said electrons drawn from said at least one emitter.

43. (original) A field emission display in accordance with claim 42, further comprising a phosphor layer applied to said anode, said phosphor layer being responsive to electrons attracted by said anode to emit light.

44. (original) A field emission display in accordance with claim 37, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed between said transistor and said ground potential.

45. (original) A field emission display in accordance with claim 43, further comprising, for each cathodoluminescent element, an infrared-sensitive element disposed between said transistor and said ground potential.

46. (original) A field emission display in accordance with claim 45, wherein, for each cathodoluminescent element, the intensity of said light emitted by said anode varies in relation to levels of infrared radiation to which said infrared-sensitive element is exposed.

47. (original) A field emission display in accordance with claim 46, wherein said infrared-sensitive element is platinum silicide.

48. (original) A field emission display in accordance with claim 46, wherein said array of cathodoluminescent elements is adapted to present an image corresponding to infrared radiation levels to which said display is exposed.

49. (original) A field emission display, comprising:
an array of cathodoluminescent elements each being responsive to a separate select signal to emit electrons toward an anode having a phosphor layer applied thereon, such that light is selectively emitted from said phosphor layer; and
for each cathodoluminescent element in said array, a programmable element for determining a level of voltage adjustment to said separate select signal, such that said programmable element determines the intensity of light emitted from said phosphor layer.

50. (original) A field emission display in accordance with claim 49, wherein for each cathodoluminescent element, said programmable element comprises a charge storage device for storing a level of electrical charge thereon.

51. (original) A field emission display in accordance with claim 50, wherein for each cathodoluminescent element, said charge storage device comprises a floating gate of a floating gate transistor.

52. (original) A field emission display in accordance with claim 51, further comprising, for each cathodoluminescent element, an infrared-sensitive element for modulating said intensity of light emitted by said phosphor layer in proportion to levels of infrared radiation to which said each cathodoluminescent element is exposed.

53. (original) A method of operating a field emission display comprising an array of cathodoluminescent elements each having a programmable element associated therewith and each responsive to a separate select signal having a predetermined voltage level to cause light to be emitted from said display at a respective location in said array corresponding to said each cathodoluminescent element, comprising:

- (a) separately for each cathodoluminescent element in said array, pre-programming said programmable element to specify the intensity of light emitted by said display at said respective location in said array corresponding to said each cathodoluminescent element in response to application of said separate select signal corresponding to said each cathodoluminescent element.

54. (original) A method in accordance with claim 53, wherein for each said cathodoluminescent element in said array, said step of pre-programming comprises storing a desired level of electric charge on a charge storage device.

55. (original) A method in accordance with claim 54, wherein said step of storing a desired level of electric charge comprises storing said desired level of electric charge on a floating gate of a floating gate transistor.

56. (currently amended) A field emission display device comprising:
- a p-type substrate defining an upper surface;
 - a first n-type doped region formed in said p-type substrate at said upper surface of said p-type substrate;
 - a second n-type doped region spaced from said first n-type doped region and formed in said p-type substrate at said upper surface of said p-type substrate;
 - an electrically conductive metallic film formed over said upper surface of said p-type substrate and in contact with the first and second n-type doped regions;
 - an electrically conductive grid;
 - an electrically conductive anode structure; and
 - an electron emitter conductively coupled to said first n-type doped region, wherein said at least one electron emitter and said grid are displaced from said anode structure across a field emission region.
57. (currently amended) A field emission display device as claimed in claim 56 wherein said electron emitter is formed over said first n-type doped region.
58. (currently amended) A field emission display device as claimed in claim 57 wherein said electron emitter is formed integrally with said first n-type doped region.
59. (currently amended) A field emission display device as claimed in claim 56 wherein said electron emitter comprises a tip.
60. (currently amended) A field emission display device as claimed in claim 56 wherein said anode structure comprises a phosphor coated screen.
61. (currently amended) A field emission display device as claimed in claim 56 wherein said metallic film is platinum silicide.

62. (currently amended) A field emission display device as claimed in claimed 56 wherein said metallic film and said p-type substrate comprise an infra-red-sensitive junction.

63. (currently amended) A field emission display device as claimed in claimed 56 further comprising a dielectric layer between the grid and the metallic film.

REISSUE APPLICATION: CONSENT OF ASSIGNEE; STATEMENT OF NON-ASSIGNMENT		Docket Number (Optional) <i>102 - 0155451</i>
This is part of the application for a reissue patent based on the original patent identified below.		
Name of Patentee(s) <i>Micron Technology, Inc.</i>		
Patent Number <i>6,366,266</i>	Date Patent Issued <i>4/2/02</i>	
Title of Invention <i>Method and Apparatus for Programmable Field Emission Display</i>		
<p>1. <input checked="" type="checkbox"/> Filed herein is a statement under 37 CFR 3.73(b). (Form PTO/SB/96)</p> <p>2. <input type="checkbox"/> Ownership of the patent is in the inventor(s), and no assignment of the patent is in effect.</p> <p>One of boxes 1 or 2 above must be checked. If multiple assignees, complete this form for each assignee. If box 2 is checked, skip the next entry and go directly to "Name of Assignee".</p> <p>The written consent of all assignees and inventors owning an undivided interest in the original patent is included in this application for reissue.</p>		
The assignee(s) owning an undivided interest in said original patent is/are <i>Micron Technology Inc.</i> and the assignee(s) consents to the accompanying application for reissue.		
Name of assignee/inventor (if not assigned) <i>Micron Technology, Inc.</i>		
Signature <i>Russ Slifer</i>	Date <i>8/28/06</i>	
Typed or printed name and title of person signing for assignee (if assigned) <i>Russ Slifer, Chief Patent Counsel for Assignee Micron Technology, Inc., who is authorized to execute this paper.</i>		

This collection of information is required by 37 CFR 1.172. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 6 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Tianhong Zhang, et al.

Serial No.: 10/706,486
(Reissue application of USP
6,366,266)

Filed: November 12, 2003

Entitled: **Method and Apparatus for
Programmable Field Emission Display**

§ Group Art Unit: 2629

§

§ Examiner: Duc Q. Dinh

§

§ Examiner phone: 571-272-7686

§

§

§

§

§

§

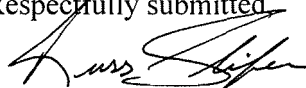
Atty. Dkt. No.: 102-0155US1

STATEMENT UNDER 37 C.F.R. § 3.73(B)

Commissioner for Patents
P.O. Box 1450
Alexandria VA, 22313-1450

Pursuant to Rule 3.73(b), I, Russ Slifer, represent that I am an authorized to act on behalf of the assignee, Micron Technology, Inc., which owns a 100% interest in the above-referenced application by virtue of the assignment recorded on August 24, 2006 at the U.S. Patent and Trademark Office at Reel 018165, Frame 0067.

Respectfully submitted



Russ Slifer
Chief Patent Counsel
for Assignee Micron Technology, Inc.

Tel. (208) 368-2940

Date: 8/28/06